

Mitchell Overdick

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EDUCATION

WESTERN WASHINGTON UNIVERSITY

BS IN ELECTRONICS ENGINEERING
MINOR IN MATHEMATICS
2017 | Bellingham, WA
Cum. GPA: 3.48

COURSEWORK

UNDERGRADUATE

Digital Circuit Design
Analog Circuit Design
Embedded Systems (C/Assembly)
Printed Circuit Boards
Digital Signal Processing
Communication Systems
(Research Assistant)
Unix Tools and Scripting
C++
SDR Programming
High-Speed DSP

SKILLS

PROGRAMMING

Over 5000 lines
C • Python • MATLAB
Over 1000 lines
C++ • C# • Java • JavaScript • JavaCard
• \LaTeX • Assembly • CSS • XAML
Familiar
MySQL • PHP • R • Bash • Batch

SOFTWARE

Versed
Altium • Gimp • MS Office • Git • OnShape
• Eclipse
Proficient
LabVIEW • LogiCAD • VisualStudio
• PyCharm • SVN • dSPACE • CANalyzer

GENERAL

3D Printing • Soldering • Wiring Harness
• Organization • Diagnostics • Time
Management • Mechanic • Graphic Arts
• Recording Engineering

SOCIETIES

2016 International IEEE HKN
2013 National NSCS

EXPERIENCE

TYFONE | SYSTEMS AND APPLICATIONS ENGINEER

Sep 2018 – Present | Portland, OR

- Flexible PCB layout for BLE enabled smart card
- Developed proofs of concept for potential products
- Developed and maintained Altium component database
- Performed duties of Field Applications Engineer
- Supported Department of Defence project

PACCAR TECHNICAL CENTER | EMBEDDED SYSTEMS VALIDATION ENGINEER

Jun 2017 – Aug 2018 | Mount Vernon, WA

- One of the original developers of test automation platform
- Serviced and maintained HIL benches
- Developed test procedures for embedded vehicle software

RESEARCH

WESTERN WASHINGTON UNIVERSITY | UNDERGRAD RESEARCH ASSISTANT

Jul 2015 – Jun 2017 | Bellingham, WA

Worked with **Joe Canfield** and **Prof. Andrew Klein** to develop a working proof of concept timestamp-free network synchronization algorithm using Ettus Research USRP series software defined radios. The research resulted in two IEEE publications at, **ICASSP** and **AeroConf**

PUBLICATIONS

- | | | |
|------|---------------|---|
| 2018 | IEEE Oceans | "A Small Energy Harvesting Autonomous Surface Vehicle" |
| 2018 | IEEE AeroConf | "Implementation and testing of a low-overhead network synchronization protocol" |
| 2017 | IEEE ICASSP | "A software-defined radio implementation of timestamp-free network synchronization" |

PROJECTS

MACHINE LEARNING | CERAMIC TRADEMARK IDENTIFICATION CNN

Oct 2017 – Apr 2018

- Worked alongside WWU Anthropology Department student
- Developed basic method for training neural network on limited training set
- Created 6000 image data set from 24 images using MATLAB
- Trained TensorFlow CNN which has achieved a preliminary accuracy of 80%
- Presented work in poster session at NWAC 2018 in Boise, ID

AUTOMATION | HIL/SIL AUTOMATION INTEGRATED PLATFORM

Jan 2018 – Sep 2018

- Helped develop fully integrated graphical test automation application
- Defined initial architecture and use cases
- Lead GUI designer, written in XAML and C#

REFERENCES

*Any and all contact information provided herein is to the individual's respective affiliation.
Direct contact information can be provided upon request.*

DR. SIVA NARENDRA | CEO - TYFONE, INC.

(503) 226-3939

JONATHAN DEHART | EXECUTIVE VICE PRESIDENT - TYFONE, INC.

(503) 226-3939

WARREN BEBOUT | SENIOR DEVOPS ENGINEER - TYFONE, INC.

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YELENA BARABANOVA | HR AND ADMINISTRATIVE COORDINATOR - TYFONE, INC.

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DYLAN KLEIST | EMBEDDED SYSTEMS ENGINEER - PACCAR TECHNICAL CENTER

(360) 757-8311

JACIE UNPINGCO | TEST DEVELOPMENT ENGINEER - PACCAR TECHNICAL CENTER

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DR. ANDREW KLEIN | PROFESSOR - WESTERN WASHINGTON UNIVERSITY

(360) 650-3000

DUSTIN SJOERDSMA | EE MASTER'S CANDIDATE - PORTLAND STATE UNIVERSITY